

## Claims

- [c1] 1. A clock control method for preventing the charge couple device from saturation, wherein the charge couple device comprises a shift register, a last stage register and a pixel processing circuit; wherein the method for preventing the charge couple device from saturation comprises the steps of:
- in a first accumulated period of a signal transmission clock, the last stage register receiving a charge voltage sent by the shift register, as an accumulated voltage;
  - in a first shift-out period of the signal transmission clock, the last stage register sending the accumulated voltage to the pixel processing circuit;
  - in a non-reset period of a reset clock, obtaining a photo intensity corresponding to the accumulated voltage sent to the pixel processing circuit;
  - in a second accumulated period of the signal transmission clock, the last stage register receiving the charge voltage sent by the shift register, as an abandoned voltage;
  - in a second shift-out period of the signal transmission clock, the last stage register sending the abandoned voltage to the pixel processing circuit; and
  - in a reset period of the reset clock, the pixel processing circuit generating a reset voltage to clear the abandoned voltage sent from the last stage register to the pixel processing circuit.
- [c2] 2. The method of claim 1, wherein the shift register sends the charge voltage to the last stage register according to either a rising edge or a falling edge of a charge shift-out clock.
- [c3] 3. The method of claim 1, wherein in the first shift-out period of the signal transmission clock, the shift register sends the charge voltage to the last stage register and the last stage register subsequently sends the charge voltage to the pixel processing circuit.
- [c4] 4. The method of claim 1, wherein in the second shift-out period of the signal transmission clock, the shift register sends the charge voltage to the last stage register and the last stage register subsequently sends the charge voltage to the pixel processing circuit.

[c5]

5. The method of claim 1, wherein the clock control method for preventing the charge couple device from saturation is the clock control method for preventing the interleaved charge couple device from saturation.